

**DUAL PAGING CHANNEL RECEIVER FOR  
A WIRELESS COMMUNICATIONS SYSTEM**

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**BACKGROUND OF THE INVENTION**

Field of Invention:

10        This application is a non-provisional application claiming priority to  
provisional application Serial No. 60/176,466, filed on January 17, 2000.  
This invention relates to wireless communications systems. Specifically, the present  
invention relates to receivers for demodulating quick paging channels in  
communications systems employing more than one paging channel to facilitate offline  
15        processing.

Description of the Related Art:

Wireless communications systems are employed in a variety of demanding  
20        applications ranging from search and rescue to Internet applications. Such  
applications require reliable, cost-effective, and space-efficient communications  
systems with accompanying wireless phones having maximum battery life and  
associated standby time.

Cellular telecommunications systems, such as Code Division Multiple access  
25        (CDMA) communications systems, are often characterized by a plurality of mobile  
stations (e.g. cellular telephones, mobile units, wireless telephones, or mobile phones)  
in communication with one or more Base Station Transceiver Subsystems (BTS's).  
Signals transmitted by the mobile stations are received by a BTS and often relayed to  
a Mobile Switching Center (MSC) having a Base Station Controller (BSC). The  
30        MSC, in turn, routes the signal to a Public Switched Telephone Network (PSTN) or to

another wireless phone. Similarly, a signal may be transmitted from the Public Switched Telephone Network to a wireless phone via a base station or BTS and an MSC.

Wireless communications networks often employ various channels, such as  
5    paging channels and traffic channels, as disclosed in the IS-95 cellular telephone  
standard, to facilitate communications between a wireless phone and a BTS. Paging  
messages are transmitted over a paging channel by a BTS to an associated wireless  
phone to indicate an incoming call. When a wireless phone detects a paging message,  
a sequence of service negotiation messages is transmitted between the wireless phone  
10    and an associated BTS to establish a traffic channel. A traffic channel typically  
supports voice and data traffic.

Conventionally, a wireless telephone continuously monitors the paging  
channel for pages indicative of incoming calls. The receiver of the wireless phone  
remains on while signal processing circuitry within the wireless phone demodulates  
15    the paging channel to determine if a page was sent. Unfortunately, the receiver draws  
excess power, which significantly limits phone battery life.

Systems for minimizing wireless phone power consumption are often  
employed in the wireless phone and/or accompanying network to extend phone  
battery life, i.e., standby time. To improve standby time, some newer wireless phones  
20    operate in slotted mode. In slotted mode, the receiver of the wireless phone is  
periodically activated in accordance with predetermined paging slots established in  
accordance with the IS-95 telecommunications standard. An associated BTS  
transmits pages during the paging slots. Wireless phone standby time is extended by  
periodically powering-up the receiver and demodulating the paging channel rather  
25    than continuously demodulating the full paging channel as done previously.

Unfortunately, paging channel messages are often long and require extensive  
processing, which increases phone power consumption and reduces battery life and  
associated standby time. Furthermore, the design of such systems and the associated  
paging channels necessitates redundant processing of the lengthy paging channel  
30    messages to detect incoming calls. This further reduces phone battery life.

Further increases in phone standby time are achieved via a relatively new addition to the IS-95 telecommunications standard known as offline processing. In a wireless communications network employing offline processing, a pair of Quick Paging Channel (QPCH) symbols is periodically transmitted to the wireless phone.

5 The quick paging channel symbols, i.e., quick pages, indicate the presence or absence of an incoming call to be established on a forthcoming traffic channel (F-CCCH). The QPCH symbols arrive in pairs at 9600 bits per second (bps) or 4800 bps. The time slots at which the QPCH symbols are transmitted from an associated BTS are known by the wireless phone, which periodically powers-up the receiver at

10 corresponding time slots.

In a wireless phone employing offline processing, the wireless phone receiver powers-up, samples the QPCH, then immediately powers-down the receiver and processes the QPCH sample offline (when the receiver is off). Subsequent analysis of the QPCH sample or samples indicates whether the wireless phone should power-up

15 the receiver and demodulate the paging channel to receive an incoming page associated with an incoming call. Use of the QCPH helps minimize receiver activation time and the instances of complete paging channel demodulation, enabling a reduction in wireless phone power consumption and an associated extension in phone battery life. Unfortunately, existing systems and methods for demodulating the

20 QPCH and deciding whether or not to process the subsequent full paging channel based on the QPCH are undesirably large, expensive, consume excess power, and are generally inefficient. Furthermore, existing systems often fail to effectively employ one or both symbols of the QPCH and noise power estimates as needed to effectively determine whether to process the forthcoming full paging channel.

25 Hence, a need exists in the art for an efficient and cost effective system and method for receiving and processing quick paging channel symbols to determine whether or not to process the forthcoming full paging channel. There exists a further need for an efficient system and method that selectively employs noise power estimates and either one or both symbols of each quick paging channel slot to most

efficiently and reliably detect the presence of an incoming page via minimal requisite hardware.

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## SUMMARY OF THE INVENTION

The need in the art is addressed by the efficient system for determining if a paging channel should be received and processed of the present invention. In the illustrative embodiment, the inventive system is adapted for use a wireless communications device in a wireless communications system employing a quick paging channel. The system includes a first mechanism for receiving an electromagnetic signal having both pilot signal and quick paging signal components. A second mechanism provides one or more initial quality parameters ( $E_{pilot1}/\hat{I}_{o1}, E_{pilot1}$ ) indicative of a quality of a signal environment in which the electromagnetic signal is propagating. The one or more initial quality parameters are based on the pilot signal and are associated with a first symbol of the quick paging signal. A third mechanism ascertains whether a second symbol of the quick paging channel signal or the subsequent paging channel should be processed based on the one or more initial quality parameters and provides a first indication in response thereto. A fourth mechanism determines if the subsequent paging channel should be processed based on a second quality parameter ( $E_{pilot2}/\hat{I}_{o2}$ ) and a combined decision metric (demodulation symbol ( $D$ )) associated with both the first symbol and the second symbol when the first indication indicates that the second symbol should be processed. The fourth mechanism provides a second indication response thereto.

In a specific embodiment, the subsequent paging channel is a primary paging channel. The quick paging channel signal includes a dual slot having the first symbol and the second symbol. The third mechanism includes a mechanism for comparing a first quality parameter of the one or more initial quality parameters to a first

comparison threshold and providing the first indication in response thereto. The first quality parameter ( $CSI_1$ ) is computed in accordance with the following equation:

$$CSI_1 = \frac{E_{pilot1}}{\hat{I}_{ol}},$$

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where  $E_{pilot1}$  is the energy of the portion of the pilot signal that is received simultaneously with the first symbol, and  $\hat{I}_{ol}$  is total the energy of the portion of the received electromagnetic signal, including noise and interference, received simultaneously with the first symbol.

10 The third mechanism further includes a mechanism for providing a third indication indicative as to whether the wireless communications device should be placed in a sleep state based on a comparison of a first decision metric ( $D_1$ ) to a second decision threshold ( $T_{1/0}$ ). The first decision metric  $D_1$  is computed in accordance with the following equation:

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$$D_1 = \frac{QP_1}{E_{pilot1}}$$

where  $D_1$  is the first decision metric;  $QP_1$  is the dot product, cross product, or a combination thereof (depending on the mode of the mobile station) of the first symbol  
20 with an estimate of the pilot signal associated with the first symbol; and  $E_{pilot1}$  is an energy of the pilot signal associated with the first symbol.

The third mechanism further includes a mechanism for placing the wireless communications device in a sleep state when the third indication indicates that the wireless communications device should be placed in a sleep state. Another  
25 mechanism compares a third quality parameter ( $E_{pilot1}$ ) to a third decision threshold ( $T_{direct-page}$ ) when the third indication indicates that the wireless phone should not be immediately placed in a sleep state and provides the first indication in response thereto. The first indication indicates whether the subsequent paging channel should

be processed or whether the second symbol of the quick paging channel should be processed. The third quality parameter is representative of an energy of the pilot signal associated with the first quick paging channel signal.

The fourth mechanism includes a mechanism for using the second quality  
 5 parameter and a fourth comparison threshold to provide the second indication. The second quality parameter ( $CSI_2$ ) is computed in accordance with the following equation:

$$CSI_2 = \frac{E_{pilot2}}{Io_2}$$

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where  $Io_2$  is a total received signal energy of a first portion of the received electromagnetic signal associated with the second symbol;  $E_{pilot2}$  is an estimate of the energy of the pilot signal associated with the first portion.

The combined decision metric, i.e., demodulation symbol  $D$ , is described by  
 15 the following equation:

$$D = \frac{QP_1 + QP_2}{E_{pilot1} + E_{pilot2}},$$

where  $QP_2$  is the dot product, cross product, or a combination thereof (depending on  
 20 the mode of the mobile station) of the second symbol with an estimate of the pilot signal associated with the second symbol, and  $E_{pilot2}$  is an energy of the second portion of the pilot signal.

The novel design of the present invention is facilitated by the first, second, and third mechanisms, which strategically process the first quick paging channel symbol  
 25 and/or the second paging channel signal as needed. This avoids sometimes unnecessary processing of the second quick paging channel symbol, yet provides for a maximum probability of successful detection of a forthcoming primary paging channel. Furthermore, the use of unique metrics ( $D_1$  and  $D$ ), which employ noise

power estimates, provide more accurate representations of values of the quick paging channel symbols, which further facilitates detection of a forthcoming primary page.

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## BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a diagram of an exemplary wireless communications system constructed in accordance with the teachings of the present invention.

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Fig. 2 is a more detailed diagram of the mobile station of Fig. 1 showing a unique Quick Paging Channel (QPCH) combiner and QPCH detector constructed in accordance with the teachings of the present invention.

Fig. 3 is a flow diagram of a method implemented by the mobile station of Fig. 2 via the QPCH combiner and QPCH detector of Fig. 2.

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## DESCRIPTION OF THE INVENTION

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While the present invention is described herein with reference to illustrative embodiments for particular applications, it should be understood that the invention is not limited thereto. Those having ordinary skill in the art and access to the teachings provided herein will recognize additional modifications, applications, and embodiments within the scope thereof and additional fields in which the present invention would be of significant utility.

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Fig. 1 is a block diagram of an exemplary wireless communications system 10 for which the present invention is adapted. The system 10 includes a Mobile Switching Center (MSC) 12 having a Base Station Controller (BSC) 14. A Public Switched Telephone Network (PSTN) 16 routes calls from telephone lines and other networks and communications devices (not shown) to and from the MSC 12. The

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MSC 12 routes calls from the PSTN 16 to and from a first BTS 18 and a second BTS 20 associated with a first cell 22 and a second cell 24, respectively. The BTS's 18 and 20 are often called cell controllers.

5 The MSC 12 routes calls between the BTS's 18 and 20. The first BTS 18 directs calls to the first mobile station 26 within the first cell 22 via a first communications link 28. The communications link 28 is a two-way link having a forward link 30 and a reverse link 32. Typically, when the BTS 18 has established voice communications with the mobile station 26, the link 28 is characterized as a traffic channel. While only two BTS's 18 and 20 are shown in Fig. 1, more BTS's or  
10 fewer BTS's may be employed without departing from the scope of the present invention.

When the mobile station 26 moves from the first cell 22 to the second cell 24, the mobile station 26 is handed off to the second BTS 20. Handoff typically occurs in an overlap region 36 where the first cell 22 overlaps the second cell 24. In a soft  
15 handoff, the mobile station 26 establishes a second communications link 34 with the target BTS 20 in addition to the first communications link 28 with the source BTS 18. During a soft handoff, both the first link 28 and the second link 34 are maintained simultaneously. After the mobile station 26 has crossed into the second cell 24, it may drop the first communications link 28. In a hard handoff, the communications  
20 link 34 is not established. When the mobile station 26 moves from the first cell 22 to the second cell 24, the link 28 to the source BTS 18 is dropped and a new link is formed with the target BTS 20.

Fig. 2 is a more detailed diagram of the wireless phone, i.e., mobile station 26 of Fig. 1 showing a unique Quick Paging Channel (QPCH) combiner (demodulation symbol (D) computer) 40 and QPCH detector 42 constructed in accordance with the  
25 teachings of the present invention. For clarity, various components are omitted from Fig. 2, such as Intermediate Frequency (IF) to baseband converters, mixers, downconverters, oscillators, timers, power supplies, and amplifiers, however those skilled in the art will know where and how to implement the additional requisite  
30 components.



The mobile station 26 includes a transceiver 44 having an antenna 46 that is connected to a duplexer 48. The duplexer 48 is connected to an input of a CDMA receiver section 50 and to an output of a CDMA transmitter 52. A baseband processor 54 is connected to the CDMA transceiver 44 and includes a noise estimator 38, a controller 56, a sample Random Access Memory (RAM) 58, an interpolator 60, a searcher 62, a received energy estimator 64, a despreader/discover circuit 66, a Pilot Estimator (pilot filter) 68, a pilot energy computation circuit 70, a demodulator 72, the QPCH combiner 40, the QPCH page detector 42, a Viterbi decoder 74, QPCH memory 80, and an encoder 76.

The controller 56 is connected to a bus 78 that provides control input to the CDMA transmitter 52 and the CDMA receiver 50. An output of the CDMA receiver 50 is a digital receive signal that is provided as input to the sample RAM 58 of the baseband processor 54. An output of the sample RAM 58 is input to the interpolator 60. An output of the interpolator 60 is connected to inputs of the searcher 62, and the despreader/discover circuit 66. An output of the searcher 62 represents peaks corresponding to candidate pilot signals, which are input to the controller software/circuitry 56. A pilot output of the despreader/discover circuit 66 represents a pilot signal estimate(s) that has  $k$  in-phase ( $I_{pilot_k}$ ) and quadrature ( $Q_{pilot_k}$ ) signal components, one  $I_{pilot_k}$  and  $Q_{pilot_k}$  component for each  $k^{th}$  multipath signal component.

The pilot output of the despreader/discover circuit 66 provides input to the pilot estimator (pilot filter) 68 and the noise estimator 38. An output of the noise estimator 38 is input to the QPCH combiner 40 and represents noise variance estimates  $\sigma_{1k}^2$  and  $\sigma_{2k}^2$  associated with the first QPCH symbol and second QPCH symbol of a slot, respectively, for  $k$  detected multipath QPCH signal components. The output of the pilot estimator 68 represents a filtered pilot estimate(s) and is input to the demodulator 72 and the pilot energy computation circuit 70. An output of the pilot energy computation circuit 70 is connected to an input of the QPCH combiner 40. The peaks are provided to software running on the controller 56, which then performs

application-specific processing and provides the results to the combiner 40 for combining.

Traffic/data channel, primary (full) paging channel, and QPCH channel outputs of the despreader/decover circuit 66 are input to the demodulator 72. A dot product, cross product, and/or a dot product + cross product output, and a QPCH page output of the demodulator 72 are provided as input to the QPCH combiner 40. The dot product + cross product output may be omitted and the sum computed in the QPCH combiner 40 rather than in the demodulator 72 without departing from the scope of the present invention.

Traffic and primary paging channel outputs of the demodulator 72 are provided as input to the Viterbi decoder 74 after further processing via subsystems (not shown) such as scaling circuits and de-interleaving circuits (see IS-95 specifications). An output of the decoder 74 is connected to an input of the controller 56. The QPCH combiner 40 communicates with the page detector 42, an output of which is connected to an input of the controller 56. A QPCH memory 80 receives inputs from the QPCH combiner 40 and the controller software/circuitry 56 and provides output to the page detector 42.

In operation, CDMA signals received via the antenna 46 are directed to the CDMA receiver 50 via the duplexer 48. The CDMA receiver 50 includes radio frequency to intermediate frequency conversion circuitry (not shown) for mixing the received radio frequency signals (Rx) to intermediate frequency signals. Automatic Gain Control (AGC) circuitry (not shown) adjusts the total power of the received signal to a predetermined value. Additional frequency conversion circuitry (not shown) mixes the intermediate frequency signals to analog baseband signals, which are then converted to digital base band signals via an analog-to-digital converter (not shown). The digital baseband signals include In-phase (I), Quadrature (Q), and noise signal components.

Similarly, the CDMA transmitter 52 includes frequency conversion circuitry (not shown) for converting digital input signals (having in-phase and quadrature

signal components) output from the encoder 76 to analog radio frequency signals in preparation for transmission via the antenna 46.

The sample RAM 58 in the baseband processor 54 samples the digital baseband signals received from the CDMA receiver 50 at predetermined time slots.

5 The sample RAM 58 maintains the samples in a buffer (not shown) for use by offline processing circuitry as discussed more fully below. The predetermined time slots at which the sample RAM 58 performs sampling of the received signal are determined in accordance with IS-95 telecommunications standards. The sample RAM 58 may be selectively bypassed when the mobile station 26 is not operating in slotted mode

10 via an enable signal received from the controller 56. Other systems and methods for selectively bypassing the sample RAM 58 may be employed without departing from the scope of the present invention.

The length of the signal sample taken by the sample RAM 58 is directly related to the size of the sample RAM 58. The sample RAM 58 samples the signal

15 environment, i.e., the received signal, to gather sufficient information pertaining to a QPCH of the received signal to facilitate offline processing. As discussed more fully below, the unique design of the present invention helps minimize the required size of the sample RAM 58.

An output of the sample RAM 58 is connected to the interpolator 60. The

20 interpolator 60 upconverts a digital signal output from the sample RAM 58 to a higher digital frequency. In the present specific embodiment, the rate of the digital signal output from the sample RAM 58 is equivalent to the rate of the received digital signal, which is twice the chip rate. The interpolator 60 converts the rate of the digital signal to eight times the chip rate (CHIPx8). Those skilled in the art will appreciate that the

25 exact rates of digital signals employed by the mobile station 26 are application-specific and may be determined by one skilled in the art to meet the needs of a given application.

When the sample RAM 58 has sampled the received signal, the interpolator 60 provides an up-converted digital signal having in-phase and quadrature signal

30 components to the searcher 62 and the despreader/decover circuit 66. The searcher 62

analyzes the received digital signal and outputs candidate pilot peaks (one peak for each multipath component) to the controller software/circuitry 56.

In one embodiment of the present invention, the searcher 62 is implemented in accordance with the teachings of U.S. Patent Application Serial No. 09/696,160, filed, 5 October 23, 2000, by the inventor of the present invention, entitled "EFFICIENT SYSTEM AND METHOD FOR FACILITATING QUICK PAGING CHANNEL DEMODULATION VIA AN EFFICIENT OFFLINE SEARCHER IN A WIRELESS COMMUNICATIONS SYSTEM ", (Atty. Docket No. D990319), assigned to the assignee of the present invention and incorporated herein by reference. Alternatively, 10 the searcher 62 may be implemented as a pilot despreader that may be constructed by one skilled in the art with access to the present teachings, without departing from the scope of the present invention.

With predetermined knowledge of the total received signal energy as set by AGC circuitry (not shown) in the receive chain 50, the noise estimator 38 estimates 15 the noise associated with the first QPCH symbol and second QPCH symbol of a slot and outputs noise variance estimates  $\sigma_{1k}^2$  and  $\sigma_{2k}^2$ , respectively, for each  $k^{th}$  multipath signal component in response thereto. The noise variance estimates  $\sigma_{1k}^2$  and  $\sigma_{2k}^2$  may be computed via methods known in the art.

The pilot estimator 68 is implemented as a Finite Impulse Response Filter 20 (FIR) or an Infinite Impulse Response Filter (IIR). The pilot estimator 68 filters noise from the noisy pilot signal provided by the searcher 62 and provides a pilot signal estimate ( $\hat{P}_k$ ) in response thereto. The pilot signal estimate  $\hat{P}_k$  includes in-phase ( $I_{pilot_k}$ ) and quadrature ( $Q_{pilot_k}$ ) signal components associated with the  $k^{th}$  pilot multipath signal component and is represented by the following ( $\hat{P}_k$ ):

25

$$\hat{P}_k = (I_{pilot_k}, Q_{pilot_k}), \quad [1]$$

An additional subscript, such as 1 or 2 is added to specify whether a given signal component corresponds to a first symbol or a second symbol, respectively, of a

slot of a received QPCH signal. For example,  $\hat{P}_{1_k} = (I_{pilot1_k}, Q_{pilot1_k})$  refers to the  $k^{\text{th}}$  multipath pilot estimate associated with the first QPCH symbol. A pilot signal is associated with or corresponds to a QPCH symbol when the pilot signal is received approximately simultaneously with the QPCH symbol and is provided in the same signal sample of the sample RAM 58.

The pilot signal estimate  $\hat{P}_k$  is provided to the demodulator 72 and the pilot energy computation circuit 70. The pilot energy computation circuit 70 squares the pilot signal estimate  $\hat{P}_k$  and provides an estimate of the energy ( $E_{pilot_k}$ ) of the  $k^{\text{th}}$  pilot multipath signal component to the QPCH combiner 40. The pilot energy  $E_{pilot_k}$  includes a first component  $E_{pilot1_k}$  associated with the first QPCH symbol of a QPCH slot and a second component  $E_{pilot2_k}$  associated with the second QPCH symbol of the QPCH slot. The QPCH combiner 40 includes an integrator (not shown) for summing the pilot energies  $E_{pilot1_k}$  and  $E_{pilot2_k}$  over the  $k$  pilot multipaths to yield  $E_{pilot1}$  and  $E_{pilot2}$ , respectively, in accordance with the following equations:

$$E_{pilot1} = \sum_k E_{pilot1_k} , \quad [2]$$

$$E_{pilot2} = \sum_k E_{pilot2_k} , \quad [3]$$

where  $E_{pilot1_k}$  is the pilot energy associated with the  $k^{\text{th}}$  multipath signal component of the first QPCH symbol of a QPCH slot, and  $E_{pilot2_k}$  is the pilot energy associated with the  $k^{\text{th}}$  multipath signal component of the second QPCH symbol of the QPCH slot.

The noise variance estimates  $\sigma_{1k}^2$  and  $\sigma_{2k}^2$  output from the noise estimator 38 are employed by the QPCH combiner 40 to compute noise power estimates  $\sigma_1^2$  and  $\sigma_2^2$  for use in computing the demodulation symbol decision metric  $D$  as discussed

more fully below. The noise power estimates  $\sigma_1^2$  and  $\sigma_2^2$  are described by the following equations:

$$\sigma_1^2 = \frac{\sum_k \sigma_{1k}^2 E_{pilot1k}}{E_{pilot1}}, \quad [4]$$

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$$\sigma_2^2 = \frac{\sum_k \sigma_{2k}^2 E_{pilot2k}}{E_{pilot2}}, \quad [5]$$

where the various symbols are as described above.

10 The despreader/decover circuit 66 includes a pseudo-noise despreader (not shown) and an  $M$ -ary Walsh decover circuit (not shown) for discovering a pilot channel, a data channel, a primary paging channel, and a QPCH from the received signal output from the interpolator 60, if they exist in the received signal.  $M$  is 64 in the present embodiment. The discovered channels are provided to the demodulator 72.

15 The demodulator 72 computes the dot product, cross product, or both (depending on the communications mode of the system 26 as discussed more fully below) between a QPCH signal received from the despreader/decover circuit 66 and the pilot estimate  $\hat{P}$  output from the pilot estimator 68. In the present specific embodiment, the QPCH signal includes a slot having a first symbol and a second  
20 symbol defined in accordance with the IS-95 telecommunications standard.

The dot product ( $dot_1$ ) of the first QPCH symbol (QPCH1) with the corresponding pilot estimate  $\hat{P}_1$  is defined in accordance with the following equation:

$$dot_1 = \sum_k (I_{pilot1k} I_{QPCH1k} + Q_{pilot1k} Q_{QPCH1k}), \quad [6]$$

25

where  $k$  is the number of available multipath components of the received signal;  $I_{pilot1k}$  is the in-phase component of the pilot estimate associated with  $k^{\text{th}}$  multipath

component of the first QPCH symbol of the slot;  $I_{QPCH1_k}$  is the in-phase component of the  $k^{\text{th}}$  multipath component of the first QPCH symbol;  $Q_{pilot1_k}$  is the quadrature component of the  $k^{\text{th}}$  multipath component of the pilot estimate associated with the first QPCH symbol; and  $Q_{QPCH1_k}$  is the quadrature component of the  $k^{\text{th}}$  multipath component of the first QPCH symbol of the QPCH signal.

Similarly, the dot product ( $dot_2$ ) of the second QPCH symbol (QPCH2) with the corresponding pilot estimate  $\hat{P}_{2_k}$  is defined in accordance with the following equation:

$$dot_2 = \sum_k (I_{pilot2_k} I_{QPCH2_k} + Q_{pilot2_k} Q_{QPCH2_k}), \quad [7]$$

where the individual symbols are similar to those defined above for equation (6) but are associated with the second QPCH symbol of a slot rather than the first QPCH symbol of the slot.

Additional details of quick paging channels employed for the purposes of offline processing are disclosed in copending U.S. Patent Application Serial No. 08/865,650, filed May 30, 1997, by Butler, et al., entitled DUAL CHANNEL SLOTTED PAGING, (Atty. Docket No. QCPA419), assigned to the assignee of the present invention and incorporated herein by reference. Further QPCH details are disclosed in copending U.S. Patent Application Serial No. 09/252,846, filed February 19, 1999, by Agrawal, et al., entitled A METHOD AND APPARATUS FOR MAXIMIZING STANDBY TIME USING A QUICK PAGING CHANNEL, (Atty. Docket No. QCPA779), assigned to the assignee of the present invention and incorporated herein by reference.

The demodulator 72 computes the first dot product ( $dot_1$ ) associated with the first QPCH symbol, the second dot product ( $dot_2$ ) associated with the second QPCH symbol, and/or the cross products  $cross_1$  and  $cross_2$  associated with the first and second QPCH symbols, respectively, and provides the results to the QPCH combiner

40. The cross products  $cross_1$  and  $cross_2$  are defined in accordance with the following equations:

$$cross_1 = \sum_k (I_{pilot1_k} Q_{QPCH1_k} - Q_{pilot1_k} I_{QPCH1_k}), \quad [8]$$

5

$$cross_2 = \sum_k (I_{pilot2_k} Q_{QPCH2_k} - Q_{pilot2_k} I_{QPCH2_k}), \quad [9]$$

where the individual symbols are as defined above for equations (6) and (7).

Whether the demodulator 72 computes dot products and/or cross products is application-specific and depends on the mode of the system 26. For example, in 10 Multi-Carrier (1xMC) systems without Orthogonal Transmit Diversity (OTD) (1xMC non OTD), the demodulator 72 computes dot and cross products in accordance with equations (4) through (9) and outputs  $dot_1 + cross_1$  and  $dot_2 + cross_2$  to the QPCH combiner 40. In 3 Multi-Carrier (3xMC) systems and in 1xMC systems with OTD, 15 the demodulator 72 outputs dot products, cross products, or sums of dot and cross products depending on the needs of a given application. With reference to the present teachings, the appropriate demodulator output may be determined by one ordinarily skilled in the art to meet the needs of a given application. The additions of the dot and cross products ( $dot_1 + cross_1$  and  $dot_2 + cross_2$ ) may be performed in the QPCH 20 combiner 40 without departing from the scope of the present invention.

The output of the demodulator 72 that is input to the QPCH combiner 40 is denoted  $QP_1$  for outputs associated with the first QPCH symbol of a slot and  $QP_2$  for outputs associated with the second QPCH symbol of a slot. Various outputs of the demodulator 72 for various system modes are summarized in the following table:



Table 1

| Mode              | Quick Page Calculation (QP)  |
|-------------------|--|
| 1xMC non OTD      | $QP_1 = dot_1 + cross_1,$<br>$QP_2 = dot_2 + cross_2$  |
| 1xMC OTD, or 3xMC | $QP_1 = dot_1, cross_1, \text{ or } dot_1 + cross_1$<br>$QP_2 = dot_2, cross_2, \text{ or } dot_2 + cross_2$ |

Alternatively, another combinative function of the pilot estimate and the first  
 5 and second QPCH symbols may be provided to the QPCH combiner 40 in addition to  
 or instead of the dot and/or cross products, without departing from the scope of the  
 present invention.

The demodulator 72 may also provide a data/traffic signal, if available, to the  
 Viterbi decoder 74 when the mobile station 26 is handling a call or other type of  
 10 traffic channel. The decoder 74 may then decode the data/traffic signal, which may  
 represent voice or another type of data, and forward the decoded signal to the  
 controller 56. The controller 56 employs various hardware and/or software modules  
 (not shown) to route the decoded signals to a microphone or to another software or  
 hardware function (not shown).

15 The QPCH combiner 40 computes a first decision parameter ( $CSI_1$ ), which is a  
 carrier signal to interference ratio, also called the normalized pilot energy, and is  
 described by the following equation:

$$CSI_1 = \frac{E_{pilot1}}{\hat{I}_{o1}}, \quad [10]$$

20

where  $CSI_1$  is the normalized pilot energy associated with the first QPCH symbol of a  
 slot;  $E_{pilot1}$  is the energy of the portion of the pilot signal summed over all multipath

components and received simultaneously with the first QPCH symbol;  $\hat{I}_{o1}$  is the total energy of the portion of the received signal, including noise and interference, received simultaneously with the first QPCH symbol.

Similarly, the QPCH combiner 40 computes, as needed, a second decision  
 5 parameter  $CSI_2$  for the second QPCH symbol of a slot in accordance with the following equation:

$$CSI_2 = \frac{E_{pilot2}}{\hat{I}_{o2}}, \quad [11]$$

10 where the symbols are as described above for equation (10) but are associated with the second QPCH symbol of a slot.

In the present specific embodiment,  $\hat{I}_{o1}$  and  $\hat{I}_{o2}$  are predetermined via AGC circuitry and Gain Control Amplifiers (GCA's) (not shown) in the CDMA receive chain 50, however,  $\hat{I}_{o1}$  and  $\hat{I}_{o2}$  may be estimated via energy estimators or determined  
 15 via other mechanisms without departing from the scope of the present invention.

A third decision parameter  $D_1$  is a novel decision metric representative of the value of the first QPCH symbol of the QPCH slot that is described by the following equation:

$$D_1 = \frac{QP_1}{E_{pilot1}}, \quad [12]$$

where  $QP_1$  and  $E_{pilot1}$  are as described above.

The QPCH combiner 40 sums the parameters  $CSI_1$  and  $D_1$  over all available multipath components and provides the results to the page detector 42 when requested  
 25 by the page detector 42, which behaves in accordance with a unique method of the present invention as discussed more fully below. With access to the present teachings, those skilled in the art may build a QPCH combiner and page detector suitable for use with the present invention.

The QPCH combiner 40 employs the Quick Paging (QP) values  $QP_1$  and  $QP_2$ , the pilot energy estimates  $E_{pilot1}$  and  $E_{pilot2}$ , and received signal energy estimates  $\hat{I}_{o1}$  and  $\hat{I}_{o2}$  associated with the first and second QPCH symbols, respectively, to compute the demodulation symbol, i.e., decision metric  $D$ , when requested by the page detector 42, in accordance with the following equation:

$$D = \frac{\frac{QP_1}{\sigma_1^2} + \frac{QP_2}{\sigma_2^2}}{\frac{E_{pilot1}}{\sigma_1^2} + \frac{E_{pilot2}}{\sigma_2^2}}, \quad [13]$$

where  $D$  incorporates both the first symbol and the second symbol of the received QPCH slot and is representative of the value, either on or off, of the QPCH page corresponding to the slot;  $\sigma_1^2$  is the noise power associated with the portion of the received signal containing the first QPCH symbol;  $\sigma_2^2$  is the noise power associated with the portion of the received signal containing the second QPCH symbol; and the remaining parameters are as described above.

The page detector 42 selectively compares parameters  $CSI_1$ ,  $CSI_2$ ,  $D_1$ , and  $D$  to predetermined thresholds to determine whether the mobile station 26 should subsequently power-up the CDMA receiver 50 to receive and process a forthcoming full page sent via the primary paging channel, as discussed more fully below. When the page detector 42 determines that a forthcoming full page should be received and processed based on one or more comparisons of the above parameters ( $CSI_1$ ,  $CSI_2$ ,  $D_1$ , and  $D$ ) with predetermined thresholds, an appropriate indication is sent to the controller 56 indicating that the CDMA receiver 50 should be activated in accordance with IS-95 standards to receive and demodulate an immediately forthcoming primary paging channel. The controller 56 then activates the CDMA receiver 50 and places the sample RAM 58 in bypass mode via control signals delivered via the bus 78 at a time corresponding to a slot during which the primary paging channel is to be

received. The decoder 74 is automatically enabled via signaling information contained in the received signal.

When the mobile station 26 receives the full page on the primary paging channel, the page is despread via the despreaders/decover circuit 66, combined over  
 5 multipath components via the demodulator 72 and provided to the decoder 74, where the page is decoded. Constituent page information is forwarded from the decoder 74 to the controller 56. Software and/or hardware circuitry known in the art (not shown) within the controller 56 interprets the page. If the page indicates an incoming call associated with a forthcoming traffic channel, the controller 56 issues appropriate  
 10 control commands to various modules within the mobile station 26 to prepare the mobile station 26 to handle the forthcoming traffic channel.

If the primary paging channel should not be processed based as determined from one or more of the parameters  $CSI_1$ ,  $CSI_2$ ,  $D_1$ , and  $D$ , then an indication specifying that a full page on the primary paging channel is not forthcoming is sent to  
 15 the controller 56. The controller 56 then powers-down the transceiver section 44 and enters the mobile station 26 into a sleep state as defined in the IS-95 telecommunications standards. The QPCH is On-Off Keying (OOK) modulated, and the values of  $D_1$  and  $D$  help indicate the presence or absence (on or off, respectively) of a forthcoming paging channel.

20 Fig. 3 is a flow diagram of a method 100 implemented by the mobile station 26 of Fig. 2 via the QPCH combiner 40, memory 80, and detector 42 of Fig. 2. With reference to Figs. 2 and 3, in an initial receiving step 102, a digital received signal is output from the interpolator 60. The received signal includes a pilot signal component and a QPCH signal component that includes a first symbol of a QPCH  
 25 slot. The pilot estimator 68 outputs pilot signal components, corresponding to the first QPCH symbol, from the digital received signal and provides the pilot signal components to the pilot energy computation circuit 70, which computes and sums the pilot energies over all available multipath in accordance with equation (2) to yield  $E_{\text{pilot1}}$ . The resulting pilot energy  $E_{\text{pilot1}}$  is an estimate of the energy of the pilot signal

associated with the first QPCH symbol. Subsequently, control is passed to a signal quality step 104.

In the signal quality step 104, a normalized pilot energy ( $CSI_1$ ) ratio associated with the first QPCH symbol is computed, which is a value representative of a quality of a portion of the digital received signal containing the first QPCH symbol. The  $CSI_1$  is computed by dividing the pilot energy  $E_{\text{pilot1}}$  associated with the first symbol by the total energy of the received signal  $I_{\text{ol}}$  associated with the portion of the digital received signal containing the first QPCH symbol (see equation (10)). Subsequently, control is passed to a first erasure-checking step 106.

In the first erasure-checking step 106,  $CSI_1 = E_{\text{pilot1}}/I_{\text{ol}}$  is compared to a predetermined erasure threshold  $T_{\text{erasure}}$  that is stored in the QPCH memory 80. If  $CSI_1$  is less than  $T_{\text{erasure}}$ , then erasure is declared. When erasure is declared for the first symbol, the signal environment through which the received signal is propagating is determined to be of insufficient quality to rely on the value of the first metric ( $D_1$ ) to determine whether to receive and process a forthcoming primary paging channel.

In the present specific embodiment, the predetermined erasure threshold  $T_{\text{erasure}}$  is stored in the QPCH memory 80 associated with the page detector 42. Alternatively, the erasure threshold  $T_{\text{erasure}}$  may be provided by the controller 56 via a bus (not shown) and dynamically computed in response to a changing signal environment as indicated via the pilot energy output from the pilot energy computation circuit 70. When erasure is declared in the first erasure-checking step 106, control is passed to a pilot energy comparison step 114. Otherwise, control is passed to a first demodulation step 108.

In the first demodulation step 108, the demodulator 72 computes  $QP_1$  based on the first symbol of the QPCH page and the associated pilot signal (see table (1)) and provides the results to the QPCH combiner 40. The pilot energy computation circuit 70 provides the energy of the pilot signal ( $E_{\text{pilot1}}$ ) associated with the first QPCH symbol to the QPCH combiner 40. The QPCH combiner 40 then computes the first metric  $D_1$  in accordance with equation (12). Subsequently, control is passed to a noise power step 112.

In the noise power step 112, the QPCH combiner 40 computes and stores noise estimates  $\sigma_{1k}^2$  for available multipath signal components for possible later use in computing corresponding noise power estimates  $\sigma_1^2$  associated with the first QPCH symbol. Subsequently, control is passed to a first on-off-checking step 112.

5 In the first on-off-checking step 112, the first decision metric  $D_1$  is compared to a first on-off threshold  $T_{1/0}$ . If  $D_1$  is less than  $T_{1/0}$ , then control is passed to a sleep step 120, where the transceiver 44 is powered-down and the mobile station 26 is placed in a sleep state. If  $D_1$  is greater than  $T_{1/0}$ , then control is passed to the pilot energy comparison step 114.

10 In the pilot energy comparison step 114, the pilot energy ( $E_{\text{pilot1}}$ ) associated with the first QPCH symbol is compared to a direct-page threshold  $T_{\text{direct-page}}$  that is also predetermined and stored in the QPCH memory 80. Those skilled in the art will appreciate that  $T_{\text{direct-page}}$  may be updated via the controller 56 without departing from the scope of the present invention. If  $E_{\text{pilot1}}$  is greater than  $T_{\text{direct-page}}$ , then control is  
15 passed to a primary paging step 122, where the forthcoming primary paging channel is received and processed in accordance with IS-95 telecommunications standards. If  $E_{\text{pilot1}}$  is less than  $T_{\text{direct-page}}$ , then control is passed to a second symbol step 116.

In the second symbol step 116, steps 102, 104, and 110 are performed for the second QPCH symbol of the QPCH slot corresponding to the QPCH page of the  
20 received signal to yield values for  $QP_2$  (see table (1)) and  $E_{\text{pilot2}}$  (see equation (3)). The QPCH combiner 40 then computes  $CSI_2$  by dividing  $QP_2$  by  $E_{\text{pilot2}}$ . Subsequently, control is passed to a second erasure-checking step 118.

In the second erasure-checking step 118,  $CSI_2$  is compared to the erasure threshold  $T_{\text{erasure}}$ , which may be different from the corresponding erasure threshold  
25 employed in the first erasure-checking step 106 without departing from the scope of the present invention. If  $CSI_2$  is less than  $T_{\text{erasure}}$ , then control is passed to the primary paging step 122, where the forthcoming primary paging channel is received and processed in accordance with IS-95 telecommunications standards. Otherwise, control is passed to a second demodulation step 124.

In the second demodulation step 124, the second QPCH symbol of the QPCH slot is processed via the QPCH combiner 40 in response to control commands received from the QPCH detector 42 to yield the demodulation symbol  $D$  in accordance with equations (13). Those skilled in the art will appreciate that such  
 5 control commands may be provided by the controller 56 and the QPCH combiner 40 or the controller 56 alone instead of by the QPCH combiner 40 without departing from the scope of the present invention.

The QPCH combiner 40 computes the noise power estimates  $\sigma_1^2$  and  $\sigma_2^2$  associated with the first QPCH symbol and the second QPCH symbol of a slot of the  
 10 received signal, respectively, in accordance with equations (4) and (5), respectively. With access to the present teachings, those skilled in the art may implement a circuit or software module to compute the noise power ( $\sigma_1^2$  and/or  $\sigma_2^2$ ) without undue experimentation. The noise power estimates  $\sigma_1^2$  and  $\sigma_2^2$  may be stored in the QPCH  
 15 memory 80 and are employed by the QPCH combiner 40 to compute the decision metrics  $D$  in accordance with equation (13). Hence, in the second demodulation step 124, the QPCH combiner 40 employs noise variance estimates output via steps 110 and 116 and equations (4) and (5) to compute the corresponding noise power estimates employed to compute  $D$  in accordance with equation (13).

Subsequently, control is passed to a second on-off-checking step 126, where  $D$   
 20 is compared to a combined on-off threshold  $T_{0/1\text{combined}}$ . If  $D$  is larger than the combined on-off threshold  $T_{0/1\text{combined}}$ , then control is passed to the primary paging step 122, where the forthcoming primary paging channel is received and processed. Otherwise, control is passed to the sleep step 120 and the mobile station 26 is placed in a sleep state.

25 Thus, the present invention has been described herein with reference to a particular embodiment for a particular application. Those having ordinary skill in the art and access to the present teachings will recognize additional modifications, applications, and embodiments within the scope thereof.

It is therefore intended by the appended claims to cover any and all such applications, modifications and embodiments within the scope of the present invention.

Accordingly,

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WHAT IS CLAIMED IS:

09:43:19 "01:30:1  
T03:10" 6:29:60